SHEET 1 OF 1

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INFOR	RMATION DI	4	ATTY. DOCKET NO. SUN-P9327-MEG		APPLICATION NO. 10/637,168		
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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	OTHER DOCUM	ENTS (Including Aut	hor, Title, Date, F	ertinent P	ages, Etc.)		
/KP/	"Lock-Based Programs and Transactional Lock-Free Execution", by Ravi Rajwar et al., University of Wisconsin-Madison Technical Report #1440, April 2002.						
/KP/	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F Martinez et al., Workshop on Memory Performance Issues, International Symposium on Computer Architecture, June 2001.						
/KP/	"Transactional Memory: Architectural Support for Lock-Free Data Structures", by Maurice Herlihy.						
/KP/	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F. Martinez et al., Technical Report UIUCDCS-R-2001-2202, February 2001.						
/KP/	"Transactional Lock-Free Execution of Lock-Based Programs", by Ravi Rajwar, Proceedings of the Tenth International Conference on Architectural Support for Programming Languages and Operating Systems, Oct. 6-Oct. 9, 2002, San Jose, CA.						
EXAMINER		kkumar Patel/ (04/25	DATE CONSIDE	RED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.